GPGPU Notes:

Performance mode selection –

-gpgpu\_ptx\_sim\_mode 0

Add this to the gpgpusim.config file

-gpgpu\_ptx\_sim\_mode 1

Add this for pure functional mode.

-gpgpu\_ptx\_use\_cuobjdump 1

Add this to enable cuobjdump. For PTX PLUS

-gpgpu\_ptx\_convert\_to\_ptxplus 1

Add this to config file to support convertion from PTX to PTX PLUS

-gpgpu\_ptx\_save\_converted\_ptxplus 1

This command is used to save the PTX PLUS

Note: - GPGPU-Sim uses the predicate translation table from decuda for simulating PTXPlus instructions.

The highest bit represents the overflow flag followed by the carry flag and sign flag. The last and lowest bit is the zero flag

Files of importance:

OVERALL

1. Abstract model .cc/h – contains interface between cudasim and gpgpu sim files

CUDA SIM

1. Cuda sim.cc/h – contains interfacing of the functional and timing simulator. It contains a standalone functional simulator.
2. Instruction.cc/h -vvimp . It contains the emulation of PTX.(This is where we compute phase information).
3. Opcodes.def/h – contains Opcode information (Not sure if this is to be edited or no.)

GPGPU SIM

1. Shader.cc/h – contains the timing model for that calls the sim function.

\*\*\*\*\*\*\*\*\* Options parser – doubts \*\*